

FIG. 1

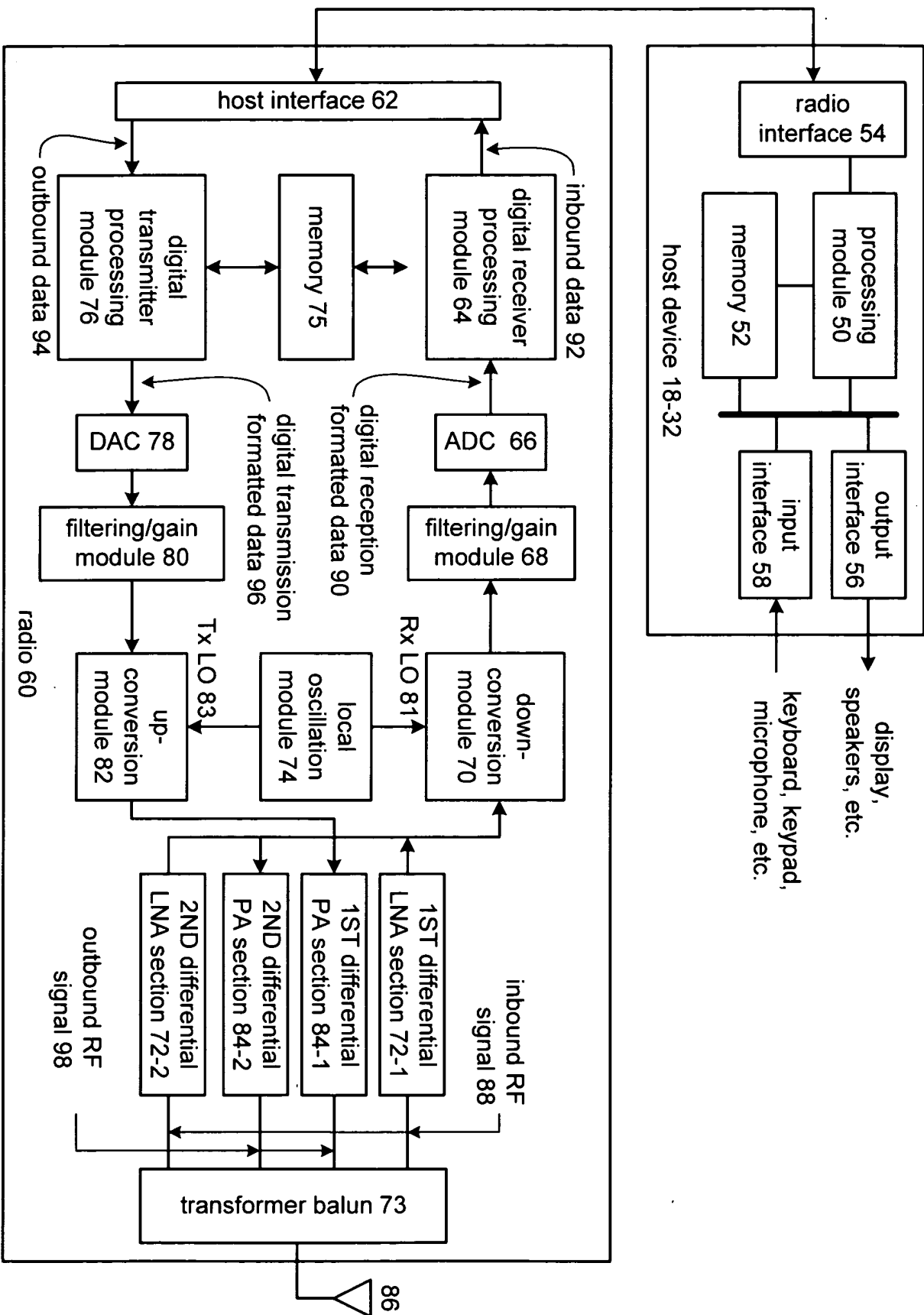


FIG. 2

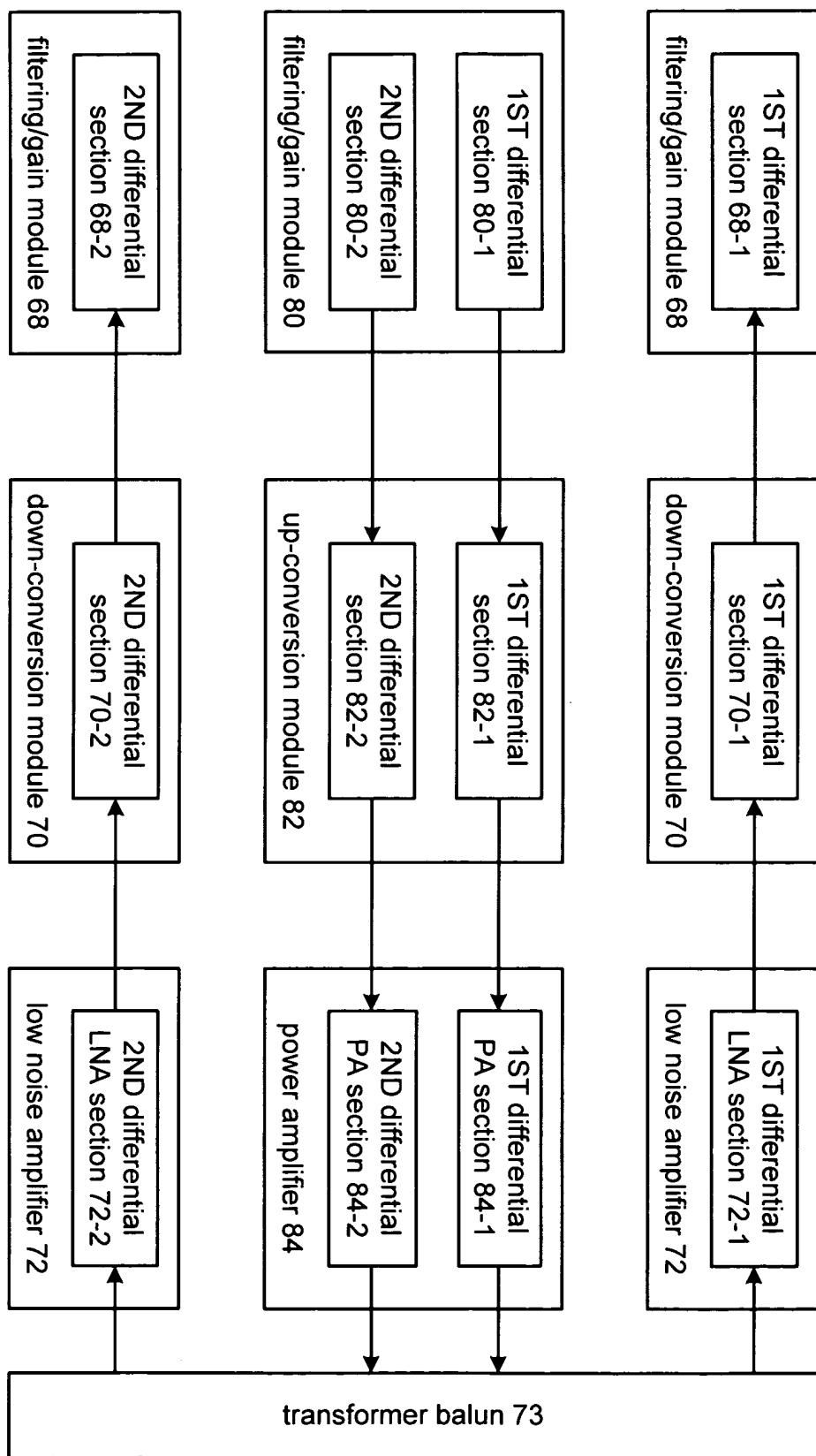
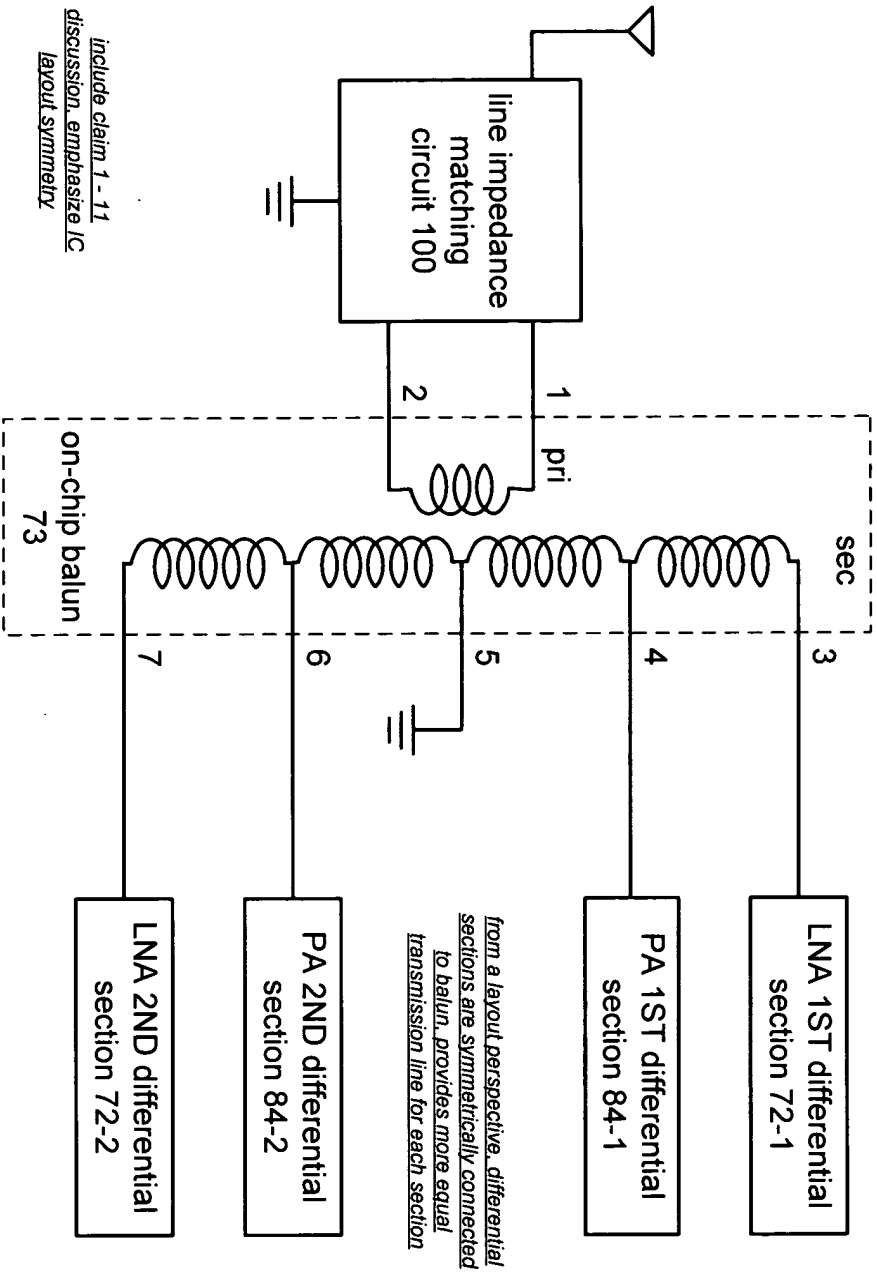


FIG. 3



include claim 1 - 11
 discussion, emphasize IC
 layout symmetry

FIG. 4

FIG. 5
 LNA 72

